International Conference on Embedded Computer Systems: Architectures, Modeling and Simulation

SAMOS XVII
Pythagoreio, Samos Island, Greece
July 17 – 20, 2017

www.samos-conference.com

SUBMISSION DEADLINE: MARCH 3, 2017

PRELIMINARY CALL FOR PAPERS

SAMOS is a unique conference. It deals with embedded systems (sort of) but that is not what makes it different. It brings together every year researchers from both academia and industry on the quiet and inspiring northern mountainside of the Mediterranean island of Samos, which in itself is different. But more importantly, it really fosters collaboration rather than competition. Formal and intensive technical sessions are only held in the mornings. A lively panel ends the formal part of the day, and leads nicely into the afternoons and evenings – reserved for informal discussions, good food, and the inviting Aegean Sea. SAMOS has IEEE technical co-sponsorship by the IEEE CAS Society and the IEEE SSCS Germany Chapter. The conference papers will be published by IEEE and included in the IEEE Xplore and DBLP Database. Authors are invited to submit technical papers in accordance to the author’s instructions describing original work.

Stamatios Vassiliadis Symposium:

This year SAMOS conference welcomes participants to celebrate the life of Stamatios Vassiliadis, the founding father of the SAMOS conference. Prof. Yale Patt, UT-Austin, chairs the half a day symposium where recognized experts in the field of embedded computer systems provide technical visionary presentations and friends of SAMOS give short remembrance talks. Presentations will be given by, among others, NIKITAS DIMOPOULOS, Univ. of Victoria, MANOLIS KATEVENIS, FORTH-ICS and Univ. of Crete, WAYNE LUK, Imperial College London, WALID NAJAR, Univ. of California - Riverside, JAMES E. SMITH, Univ. of Wisconsin Madison, PER STENSTRÖM, Chalmers Univ. of Technology, URI WEISER, Technion - Israel Institute of Technology, MATEO VALERO, Barcelona Supercomputing Center.

Applications, Systems, Architectures, and Processors:

This track focuses on advances in systems efficiency in various domains. We seek original contributions describing new architectural and micro architectural techniques aiming to improve performance (e.g. processing throughput or real-time latency), energy and power efficiency, reliability and dependability of embedded systems. We solicit novel architectures and computing methodologies and solutions for accelerating applications in various embedded domains such as next generation life sciences and medicine, next generation automotive and avionics, next generation learning systems for surveillance and recognition, immersive virtual reality. Topics of interest include (but are not limited to):

- Novel Architectures for Accelerators in High Performance Embedded Systems;
- Application-specific and Domain-specific Embedded Heterogeneous Multicore Systems;
- Embedded Reconfigurable Processors;
- Software tools, Compilation techniques and optimizations, and Code generation for Reconfigurable Architectures;
- Architecture synthesis from Functional Languages Descriptions;
- Virtualization and Energy-aware Secure, Reliable, and High Availability Multi-core Architectures;
- Embedded Parallel Systems and Multiprocessor Systems-On-Chip;
- Application level Resource Management of Multi-core Architectures;
- Memory Management and Smart Caches for big data;
- Network-on-Chip, Software Defined Network-on-Chips.

Modeling, Design, and Design Space Exploration:

This track focuses on all design processes for embedded systems ranging from system-level specification, design languages, modeling and simulation, performance, power, reliability and thermal estimation and analysis, hardware/software and system synthesis, design and design space exploration methodologies down to hardware and software synthesis and compilation strategies. Topics of interest include (but are not limited to):

- Hardware/Software and Algorithm/Architecture Co-design;
- Design Space Exploration Strategies, Algorithms and CAD Tools;
- Specification Languages and Models;
- System-Level Design, Simulation, and Verification;
- Hardware, Software and System Synthesis Techniques and CAD Tools;
- MP-SoC and Platform Based Design Methodologies;
- MP-SoC Programming, Compilers, Simulation and Mapping Technologies;
- Profiling, Measurement and Analysis Techniques and CAD Tools;
- (Design for) System Adaptivity;
- Testing and Debugging.

JOURNAL SPECIAL ISSUE. Authors of selected papers will be invited to submit an extended version of their work to contribute to a special issue of the International Journal of Parallel Programming (IJPP). This issue will accept papers from all the topics of the SAMOS conference, ranging from the Applications, Systems, Architectures, and Processors Track to the Modeling, Design, and Design Space Exploration track.

TRAVEL GRANTS. The conference will award 10 travel grants to authors and participants, who are students from Greece, Cyprus, Italy, Portugal, and Spain and, in general, to people without support from their organization.

IMPORTANT DATES.

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<td>Notification of Acceptance</td>
<td>July 17-20, 2017</td>
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